

Junior Technology Developer

Overview:

Mater Dynamics is a R&D provider company with a strong background in Chemistry, Physics, Mathematics and Electronics, that operates in the following domains: Material Sciences and Engineering, Nanotechnology and Environmental/ Life Sciences. The position for Junior Technology Developer comprises R&D activities towards hardware design and fabrication.

How to apply:

Send an email to careers@materdynamics.com. Please, explain your interest in Mater Dynamics and attach your CV. Be short and go straight to the point.

Due to the Junior status of this position, we encourage candidates to send information regarding their academic marks (for those that have none or small work experience).

Selection process:

1. After evaluating your CV, and if you are qualified for the position, we will schedule a skype meeting to go deeper into the conversation.
2. Selected candidates will pass to the next stage comprising: (i) 45 min in person meeting, and (ii) a technical challenge. Both evaluation moments will occur in the same day.
3. Finally, those that have passed the above procedure, will be interviewed in person simultaneously by two Mater Dynamics managers.

Detailed information

The Junior Technology Developer position is directed towards the execution of R&D activities related with hardware design and fabrication, interacting continuously with the CEO and the Technology Manager. The job opportunity is ideal for those that pursuit a career in embedded systems, and those having a good knowledge and understanding of circuit and designing.

The responsibilities of this position include the following:

1. **Technical Planning:** The Junior Technology Developer will be responsible to present feasible hardware solutions to be further explored, comprising also activities such as BOMs, calendarization and project follow ups.
2. **Hardware prototyping:** The candidate will conceive solutions that are aligned with project requirements. In this way, he/she will develop activities related with: (i) Embedded systems design; (ii) Microprocessor / micro-controller based design; (iii) Analog circuits, mixed signal & Power supply design; (iv) PCB layout & design, and (v) C /C++ programming for embedded systems & microprocessors.

Main Requirements

- **MSc. in a Science/Technology field:** Candidates should have a MSc in Electronics (or similar fields).
- **Languages:** It is mandatory that the candidates are capable to speak, write and read in English (US or UK).
- **IEFP eligibility:** The candidate must be eligible for an IEFP internship during the training period.

Location

The selected candidate will work at the new Mater Dynamics Lisbon Office.